

Notice of References Cited	Application/Control No. 10/613,459		Applicant(s)/Patent Under Reexamination FREY ET AL.	
	Examiner Thao P. Le		Art Unit 2818	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,261,964	07-2001	Wu et al.	438/705
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Franke et al., "Post-CMOS Modular Integration Of Poly-SiGe Microstructures Using Poly-Ge Sacrificial Layers", Solid-State Sensor and Actuator Workshop, Hilton Head, SC June 2000, pp. 18-21
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.